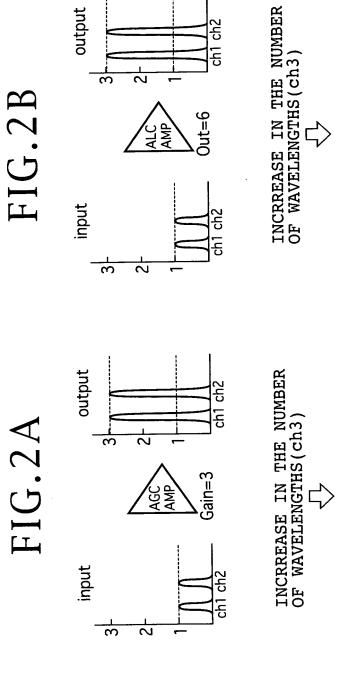
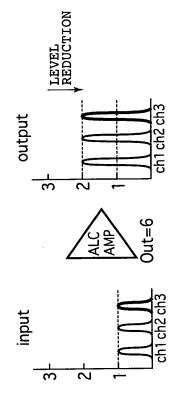
$2(\mathtt{CLT}) \sim$ PostAMP/ MUX PreAMP/ DMUX indudi 4(#4) 4(#3) SW ΝS PostAMP/ MUX PreAMP/ DMUX FIG. 1 PostAMP/ MUX PreAMP/ DMUX 4(#2) 4(#5) ΝS SW PostAMP/ MUX PreAMP/ DMUX 14 PostAMP/ MUX PreAMP/ DMUX 4(#6) ΝS SΝ 4(#1) PostAMP/ MUX PreAMP/ DMUX ∞

FIG.2A





output

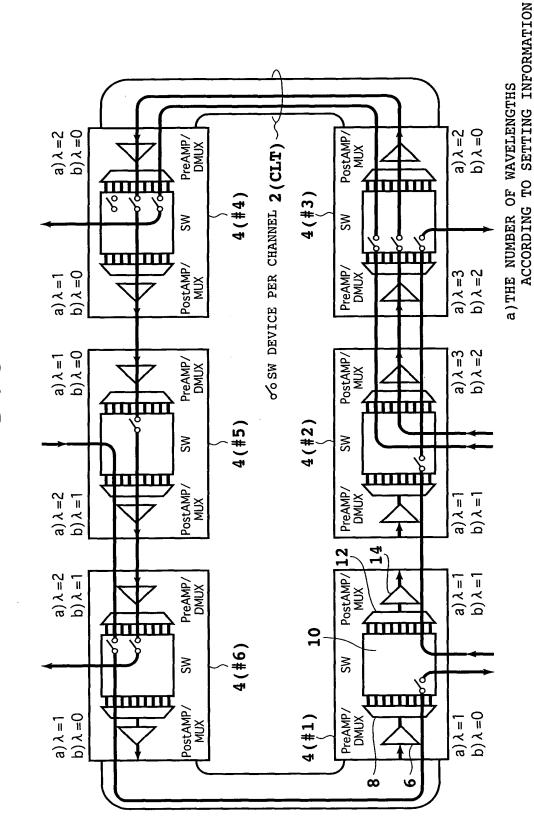
input

ch1 ch2 ch3

Gain=3

ch1 ch2 ch3

FIG.3



b) THE NUMBER OF WAVELENGTHS
ACCORDING TO SWITCH RESTRICTION

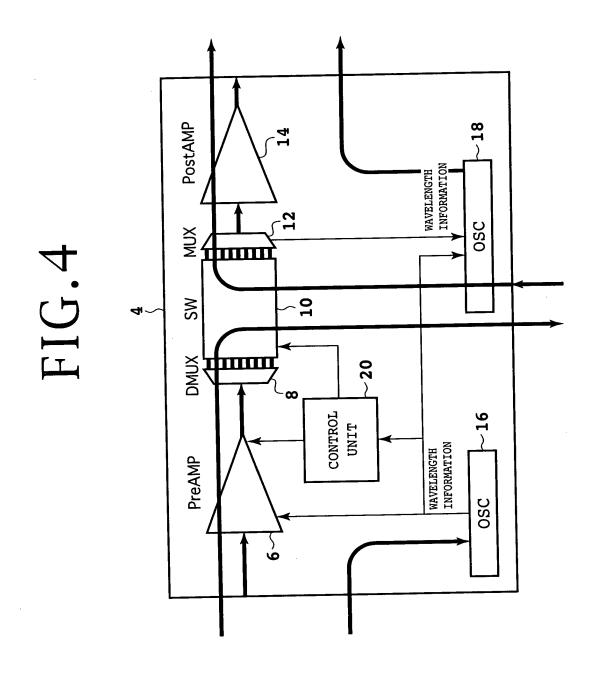
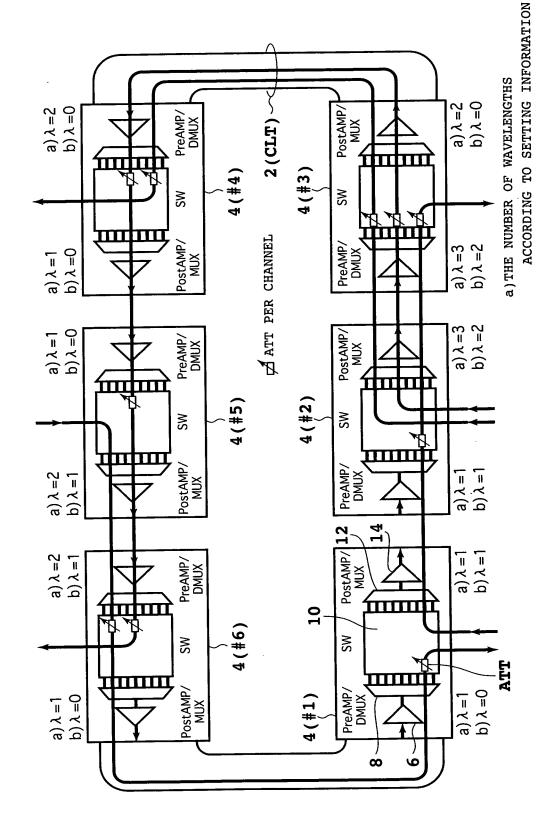
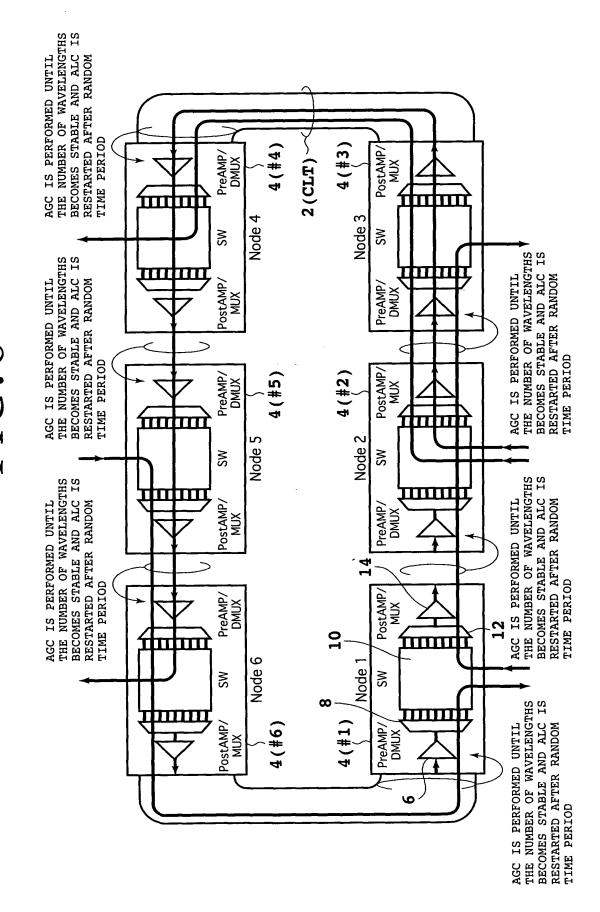


FIG.5



b) THE NUMBER OF WAVELENGTHS
ACCORDING TO ATT RESTRICTION

FIG.6



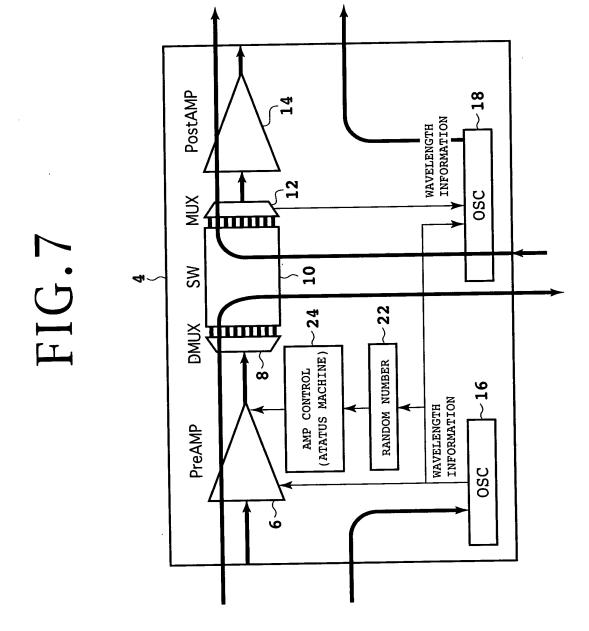


FIG.8

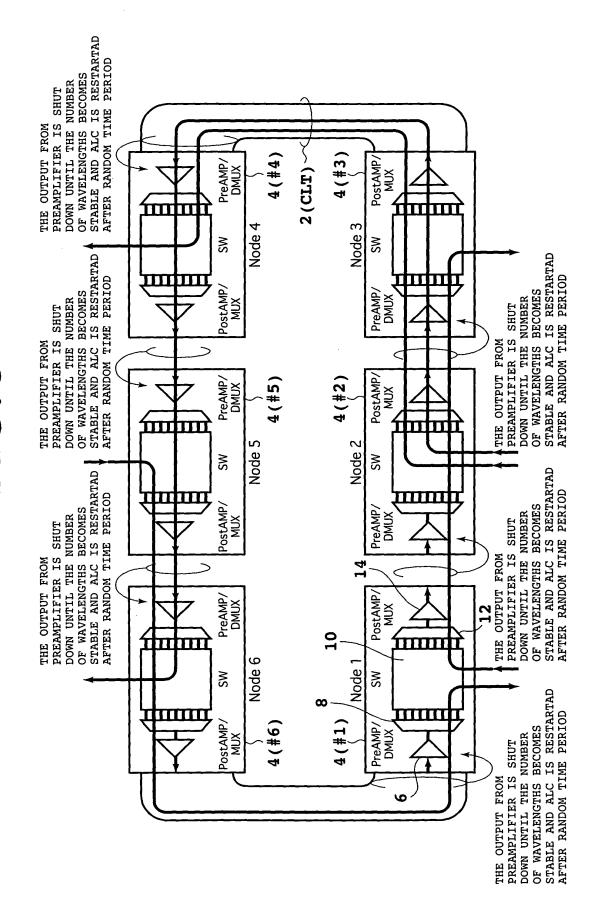
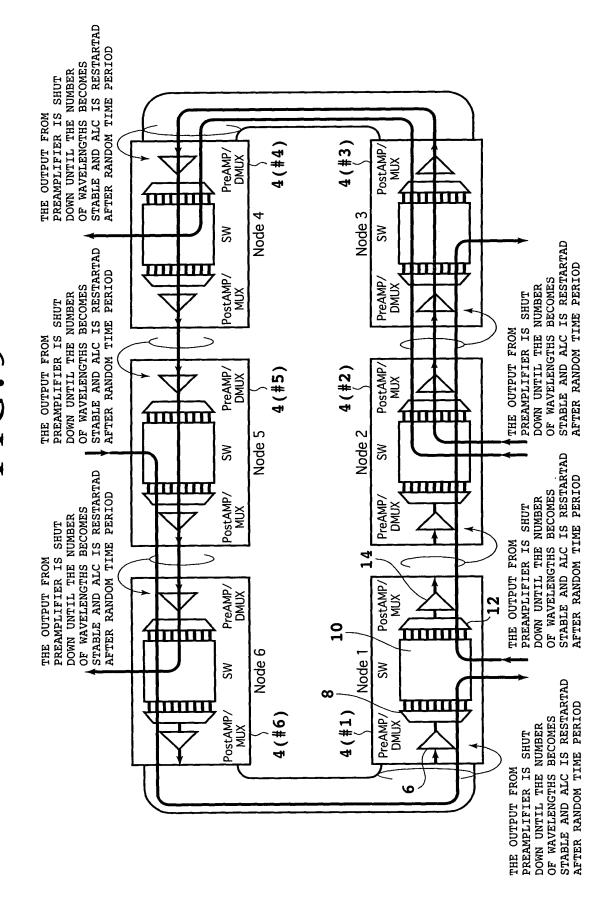


FIG.9



PostAMP WAVELENGTH INFORMATION FIG.10 OSC MCX10 SW \sim 24 ~26 DMUX ШШ (STATUS MACHINE) **8** SPECIFIC VALUE AMP CONTROL \sim 16 WAVELENGTH INFORMATION PreAMP osc